L Number	Hits	Search Text	DB	Time stamp
1	1581	framing same sequence	USPAT	2004/02/12 12:58
2	314	(framing same sequence) and (data adj packet)	USPAT	2004/02/12 12:58
3	0	((framing same sequence) and (data adj packet)) and (trannmit adj clock)	USPAT	2004/02/12 12:58
4	34	((framing same sequence) and (data adj packet)) and (transmit adj clock)	USPAT	2004/02/12 13:12
5	0	(framing same sequence) and (trasnmit adj clock)	USPAT	2004/02/12 13:13
6	83	(framing same sequence) and (transmit adj clock)	USPAT	2004/02/12 13:13
7	27	((framing same sequence) and (transmit adj clock)) and (data adj packet)	USPAT	2004/02/12 13:13
8	3	(((framing same sequence) and (transmit adj clock)) and (data adj packet)) and (clock adj period)	USPAT	2004/02/12 13:18
9	15	asynchronous and (data adj packet) and ((trannmit or master) adj clock) and (framing adj sequence)	USPAT	2004/02/12 13:19
10	18	asynchronous and (data adj packet) and ((transmit or master) adj clock) and (framing adj sequence)	USPAT	2004/02/12 13:20
11	18	asynchronous and (data adj packet) and ((transmit or master) adj clock) and (framing adj sequence) and serial$3	USPAT	2004/02/12 13:25
12	0	asynchronous and (framing adj sequence) and recover and clock and serial and (two adj transmit adj clock adj periods)	USPAT	2004/02/12 13:27
13	21	asynchronous and (framing adj sequence) and recover and clock and serial	USPAT	2004/02/12 13:32
14	0	two adj transmit adj clock adj periods	USPAT	2004/02/12 13:33
15	5	two adj transmit adj clock	USPAT	2004/02/12 13:33

C:\APPS\EAST\Workspaces\Dwin_Default.wsp

Search Results

Try the *new* Portal design

Give us your opinion after using it.
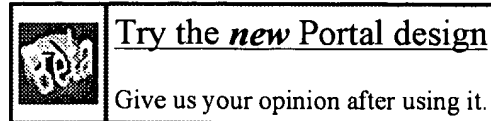
Search Results for: **[packet<AND>((data<AND>(((asynchronous AND serial AND emulation AND logic) AND (NOT ATM)))))]**
Found **96** of **127,132 searched.**

Search within Results

> Advanced Search > Search Help/Tips

Sort by: **Title** **Publication** **Publication Date** **Score** **Binder**

Results 41 - 60 of 96 **short listing**

Prev Page **1 2 3 4 5** Next Page

---

41 A structural view of the Cedar programming environment 77%

Daniel C. Swinehart , Polle T. Zellweger , Richard J. Beach , Robert B. Hagmann
ACM Transactions on Programming Languages and Systems (TOPLAS) August 1986
Volume 8 Issue 4

> This paper presents an overview of the Cedar programming environment, focusing on its overall structure—that is, the major components of Cedar and the way they are organized. Cedar supports the development of programs written in a single programming language, also called Cedar. Its primary purpose is to increase the productivity of programmers whose activities include experimental programming and the development of prototype software systems for a high-performance personal computer. T ...

42 The Alpine file system 77%

M. R. Brown , K. N. Kolling , E. A. Taft
ACM Transactions on Computer Systems (TOCS) November 1985
Volume 3 Issue 4

> Alpine is a file system that supports atomic transactions and is designed to operate as a service on a computer network. Alpine's primary purpose is to store files that represent databases. An important secondary goal is to store ordinary files representing documents, program modules, and the like. Unlike other file servers described in the literature, Alpine uses a log-based technique to implement atomic file update. Another unusual aspect of Alpine is that it performs all commu ...

43 Performance modeling and analysis: Performance analysis of time-enhanced UML diagrams based on stochastic processes 77%

Christoph Lindemann , Axel Thümmler , Alexander Klemm , Marco Lohmann , Oliver P. Waldhorst
Proceedings of the third international workshop on Software and performance July 2002

> In this paper, we propose extensions to UML state diagrams and activity diagrams in order to allow the association of events with exponentially distributed and deterministic delays. We present an efficient algorithm for the state space generation out of these UML diagrams that allows a quantitative analysis by means of an underlying stochastic process. We identify a particular stochastic process, the generalized semi-Markov process (GSMP), as the appropriate vehicle on which quantitative analysi ...

44 Session 7: Evaluating the running time of a communication round over the internet 77%

> home   > about   > feedback   > login

Try the new Portal design
US Patent & Trademark Office
Give us your opinion after using it.

Search Results

Search Results for: **[emulation<AND>((frame<AND>(((asynchronous AND serial AND clock AND recovery)))))]**
Found **26** of **127,132 searched.**

Search within Results

> Advanced Search   > Search Help/Tips

Sort by: Title   Publication   Publication Date   Score   Binder

Results 1 - 20 of 26   short listing

Prev Page **1**   **2** Next Page

---

1   Fast detection of communication patterns in distributed executions   85%
Thomas Kunz , Michiel F. H. Seuren
Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research November 1997
> Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

2   Trunking of TDM and narrowband services over IP Networks   82%
James Aweya
International Journal of Network Management January 2003
Volume 13 Issue 1
> The recent interest in IP as the vehicle for transporting TDM and narrowband services stems from the possibility of using a common transport network for voice, video, and data, and the flexibility with which new services can be introduced. A key step in the evolution of networks towards a 'broadband' IP-based environment is the 'graceful' interworking of the IP networks with the existing networks and services, particularly with the circuit switched telephone network. A &l ...

3   Multi-layer tracing of TCP over a reliable wireless link   80%
Reiner Ludwig , Bela Rathonyi , Almudena Konrad , Kimberly Oden , Anthony Joseph
ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1999 ACM SIGMETRICS international conference on Measurement and modeling of computer systems May 1999
Volume 27 Issue 1

4   The Parallel Protocol Engine   80%
Matthias Kaiserswerth
IEEE/ACM Transactions on Networking (TON) December 1993
Volume 1 Issue 6

5   Microprogrammable processor for object-oriented architecture   77%

◆IEEE

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore® RELEASE 1.6

Welcome
United States Patent and Trademark Office

Welcome to IEEE Xplore®

○ Home
○ What Can
  I Access?
○ Log-out

Tables of Contents

○ Journals
  & Magazines
○ Conference
  Proceedings
○ Standards

Search

○ By Author
○ Basic
○ Advanced

Member Services

○ Join IEEE
○ Establish IEEE
  Web Account

○ Access the
  IEEE Member
  Digital Library

Your search matched **0** of **1003743** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

asynchronous <and> emulation <and> virtual <and> clo   Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

Results:
No documents matched your query.

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore®
RELEASE 1.6

Welcome
United States Patent and Trademark Office

Help   FAQ   Terms   IEEE Peer Review | Quick Links ▼ |

» **Search Results**

Welcome to IEEE *Xplore*®

- Home
- What Can I Access?
- Log-out

Tables of Contents

- Journals & Magazines
- Conference Proceedings
- Standards

Search

- By Author
- Basic
- Advanced

Member Services

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

Your search matched **4** of **1003743** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

| serial <and> asynchronous <and> emulation | Search |

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

---

1 **Emulating ATM network impairments**
Singh, R.; Singh, M.;
Information Technology Conference, 1998. IEEE , 1-3 Sept. 1998
Pages:172

[Abstract]   [PDF Full-Text (28 KB)]   **IEEE CNF**

---

2 **An asynchronous 2-D discrete cosine transform chip**
Smith, R.; Fant, K.; Parker, D.; Stephani, R.; Ching-Yi Wang;
Advanced Research in Asynchronous Circuits and Systems, 1998. Proceedings.
1998 Fourth International Symposium on , 30 March-2 April 1998
Pages:224 - 233

[Abstract]   [PDF Full-Text (104 KB)]   **IEEE CNF**

---

3 **The architecture of MAIN-ONU for FTTC-based access-network**
Jong Oh Kim; Byung Do Ko; Jae Geun Kim;
Communications, 1999. APCC/OECC '99. Fifth Asia-Pacific Conference on ... and
Fourth Optoelectronics and Communications Conference , Volume: 1 , 18-22 Oct.
1999
Pages:23 - 26 vol.1

[Abstract]   [PDF Full-Text (360 KB)]   **IEEE CNF**

---

4 **An AAL design for circuit emulation with signalling function**
Sun Kang; Jongarm Jun; Hun Kang;
Communication Technology Proceedings, 1996. ICCT'96., 1996 International
Conference on , Volume: 2 , 5-7 May 1996
Pages:731 - 734 vol.2

[Abstract]   [PDF Full-Text (300 KB)]   **IEEE CNF**

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

IEEE Xplore®
RELEASE 1.0

Welcome
United States Patent and Trademark Office

Help   FAQ   Terms   IEEE Peer Review   Quick Links ▼   **» Search Results**

Welcome to IEEE Xplore®

○ Home
○ What Can
   I Access?
○ Log-out

Tables of Contents

○ Journals
   & Magazines
○ Conference
   Proceedings
○ Standards

Search

○ By Author
○ Basic
○ Advanced

Member Services

○ Join IEEE
○ Establish IEEE
   Web Account
○ Access the
   IEEE Member
   Digital Library

Your search matched **2** of **1003743** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

serial <and> asynchronous <and> clock <and> frame   Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

---

1 **A 622-Mb/s bit/frame synchronizer for high-speed backplane data communication**
Yoshimura, T.; Kondoh, H.; Matsuda, Y.; Sumi, T.;
Solid-State Circuits, IEEE Journal of , Volume: 31 , Issue: 7 , July 1996
Pages:1063 - 1066

[Abstract]   [PDF Full-Text (368 KB)]   **IEEE JNL**

---

2 **Single-chip 622-Mb/s SDH/SONET framer, digital cross-connect and add/drop multiplexer solution**
Baechtold, P.H.; Beakes, M.P.; Buchmann, P.; Clauberg, R.; Ewen, J.F.; Gilsdorf, J.F.; Hauviller, P.; Herkersdorf, A.; Le Garrec, J.-C.; Lemppenau, W.; Parker, B.; Pearson, D.J.; Pereira, J.M.; Plassat, D.; reynolds, S.K.; Schindler, H.R.; Steimle, A.; Webb, D.J.; Widmer, A.X.;
Solid-State Circuits, IEEE Journal of , Volume: 36 , Issue: 1 , Jan. 2001
Pages:74 - 80

[Abstract]   [PDF Full-Text (140 KB)]   **IEEE JNL**